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ATTORNEY DOCKET NO. 03180.0338
CUSTOMER NO. 22,852



Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

New U.S. Patent Application

Title: SEMICONDUCTOR WAFER AND A METHOD FOR
MANUFACTURING A SEMICONDUCTOR WAFER

Inventors' Names and Addresses:

- 1) Masao IWASE
Tokyo, Japan
- 2) Soichi NADAHARA
Tokyo, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 40 pages, including 5 independent claims and 36 claims total.
2. Drawings- 18 sheets of drawings containing 33 Figures (Figures 1A-24C).
3. The filing fee is calculated as follows:

Basic Application Filing Fee					\$770	\$ \$770.00
	Number of Claims		Basic	Extra Claims		
Total Claims	36	-	20	16	x \$18	\$ 288.00
Independent Claims	5	-	3	2	x \$86	\$ 172.00
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$290	
Subtotal						\$ 1230.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 1230.00

4. A check for \$1230.00 is enclosed. The fee includes:

\$770.00 filing fee; and
\$460.00 additional claims fee

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Patent Application Nos. 2002-305330, filed October 21, 2002 and 2003-092737, filed March 28, 2003.

Please address all correspondence with respect to this application to:

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Please accord this application an application number and filing date.

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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: 

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RVB/FPD/cma
Enclosures

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